

FIG. 1

頁社No. F008765

第所No. EP9003

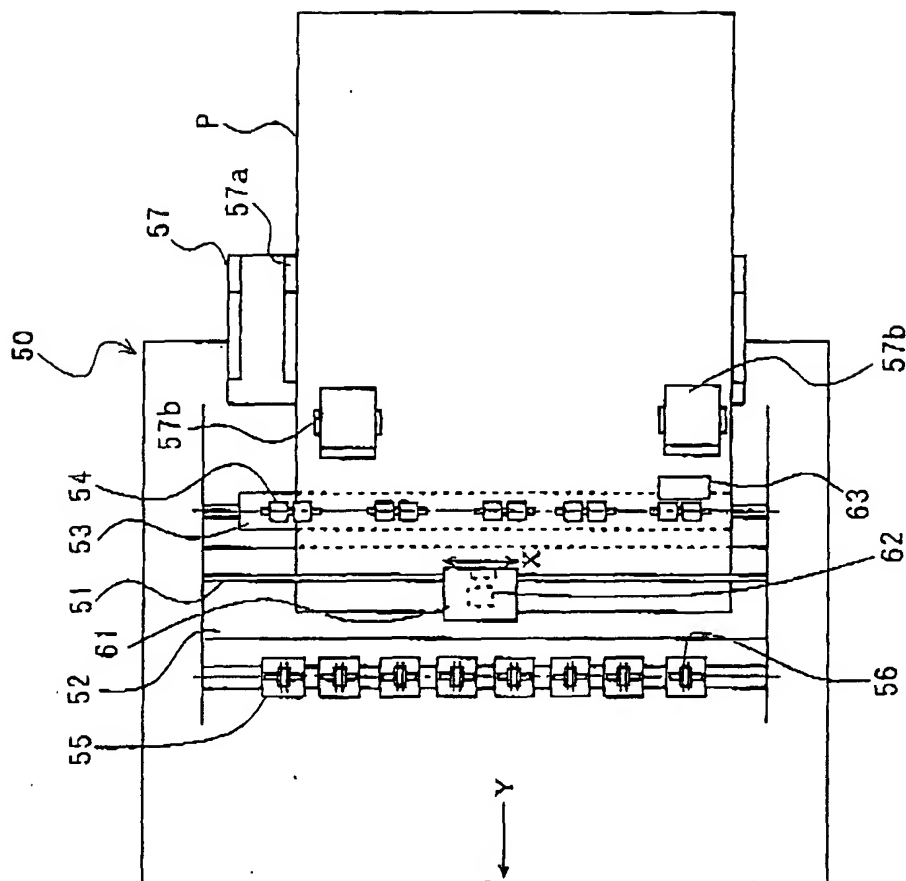


FIG. 2

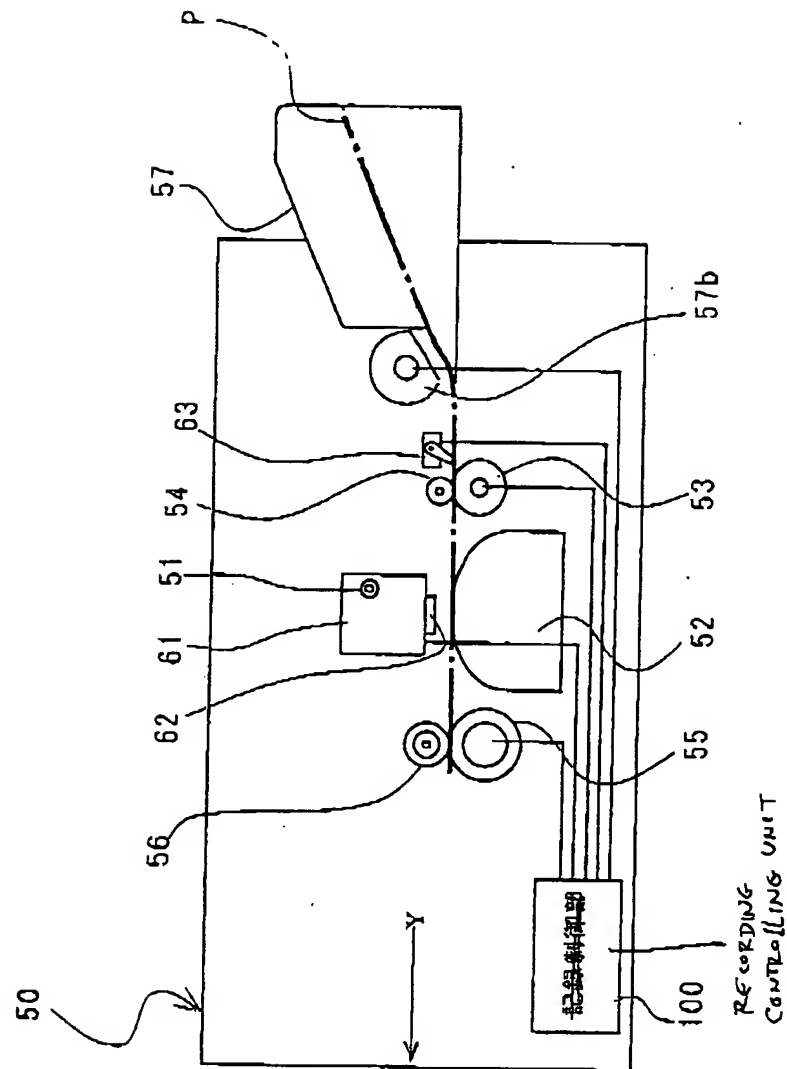
~~特許No. 7008765~~~~特許No. EP9003~~

FIG. 3

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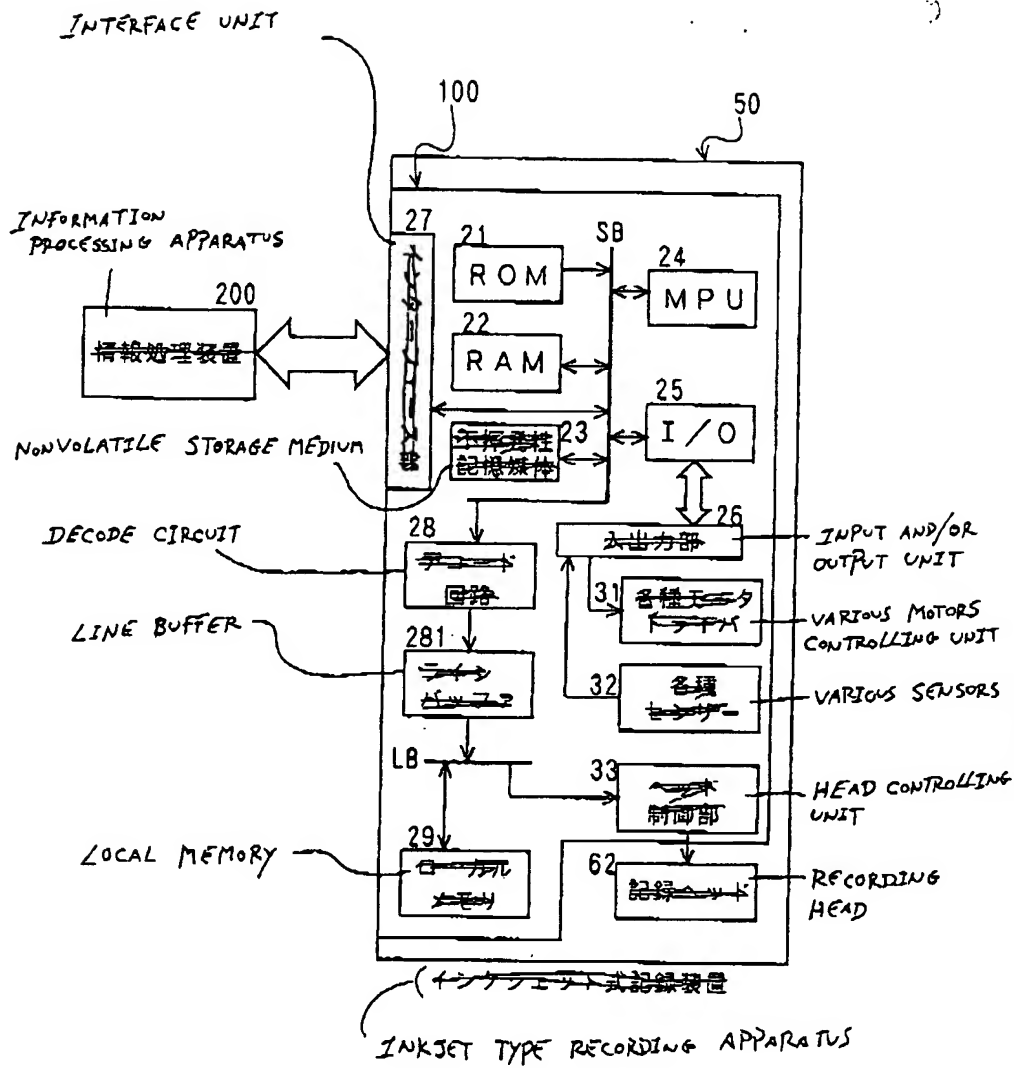


FIG. 4

頁社No. 5008765
特所No. EP9003

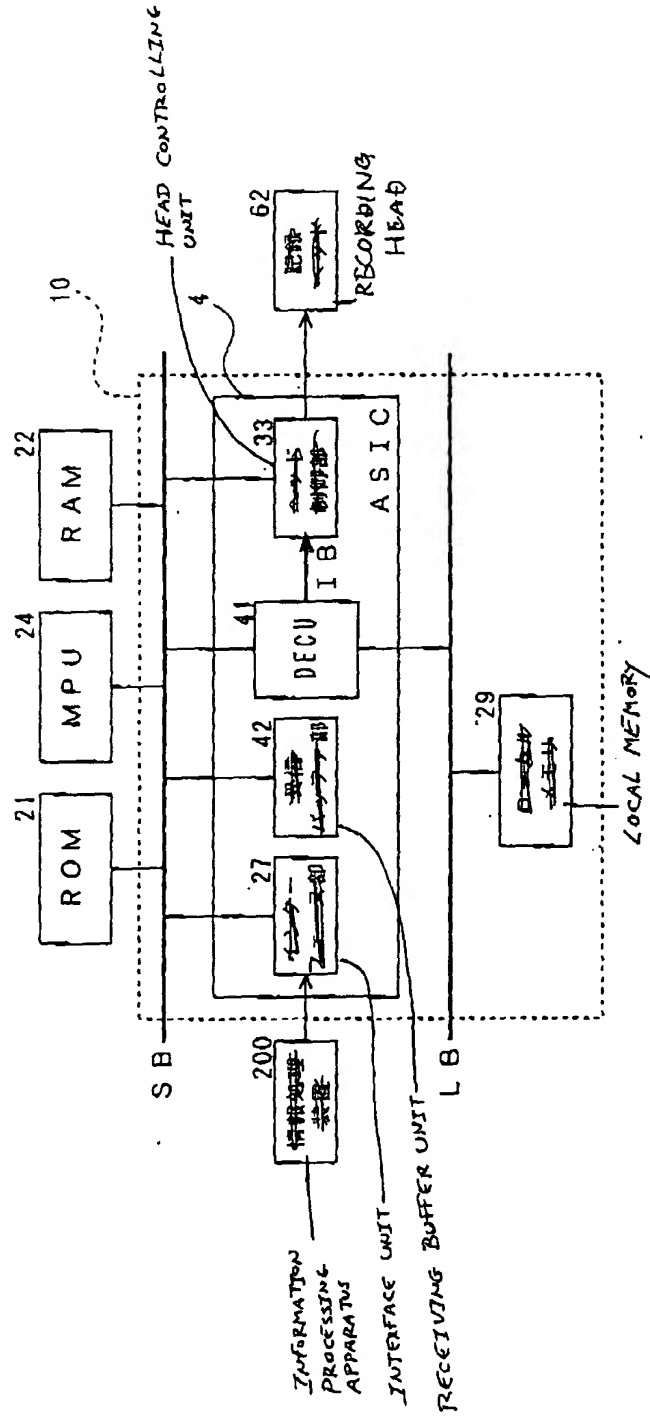


FIG. 5

貴社No. E008765

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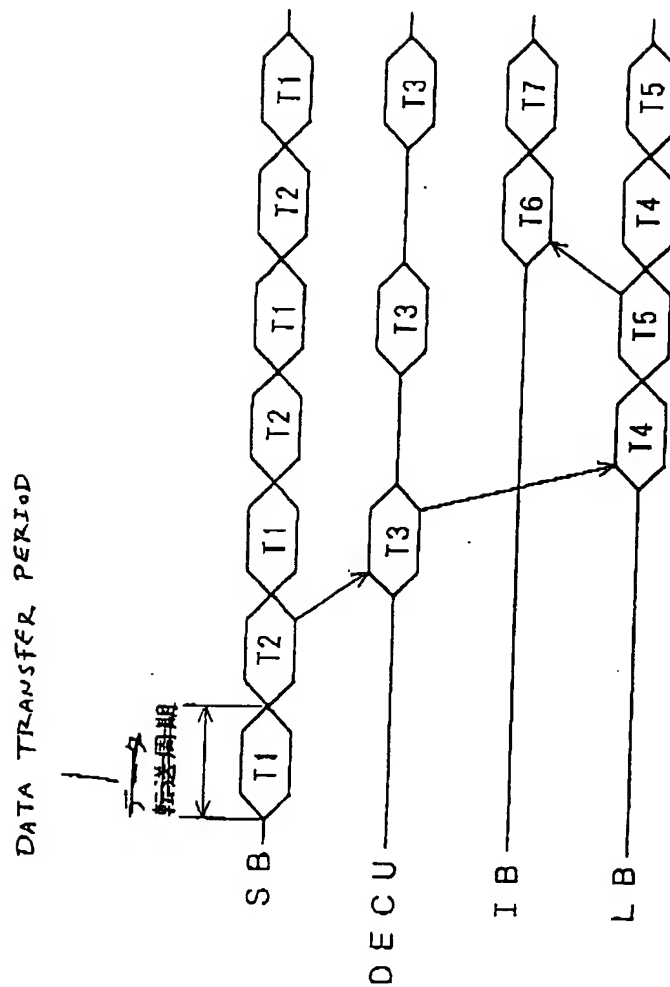
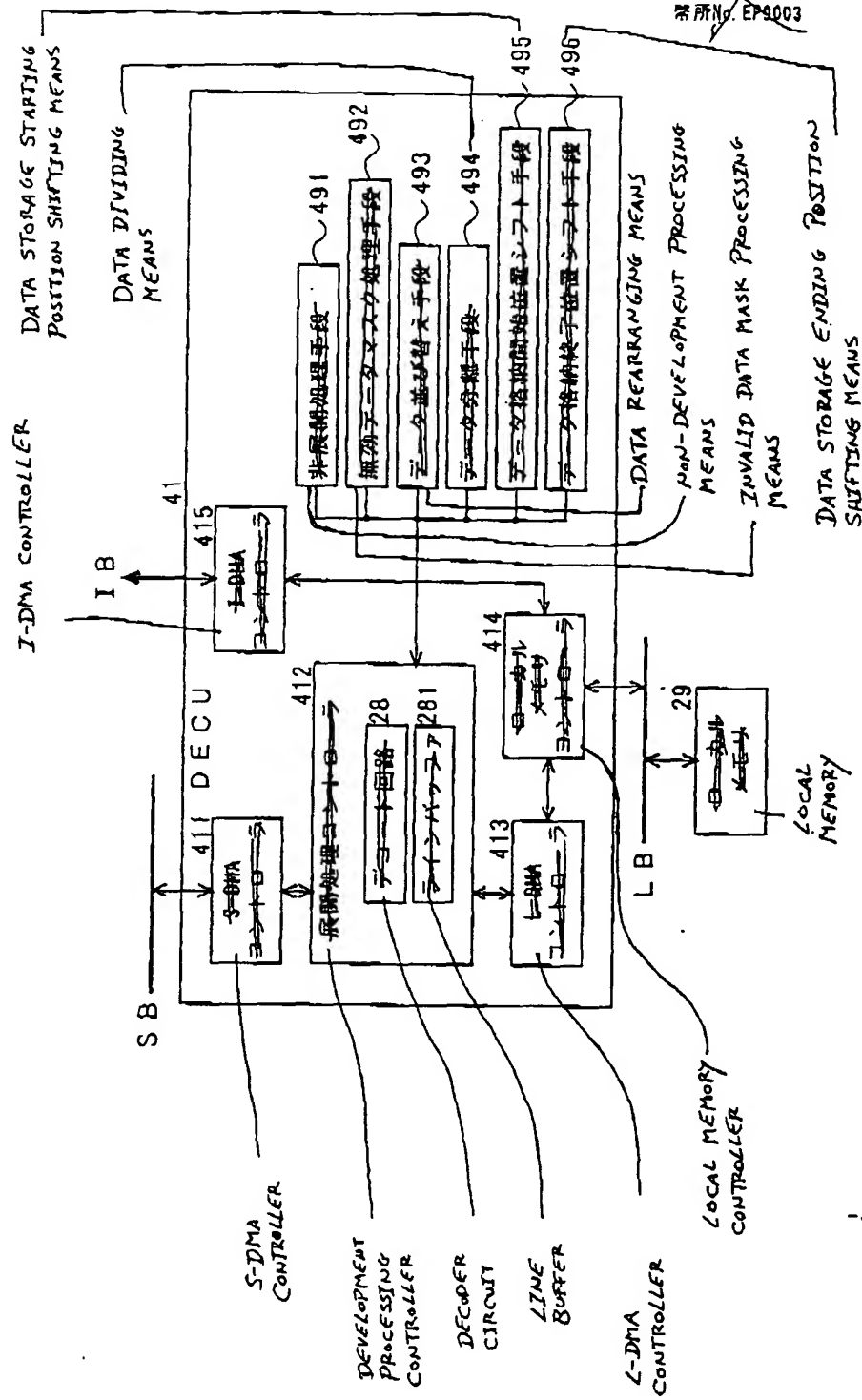


FIG. 6

貴社No. F808765

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OPERATION CONDITION

MAIN MEMORY SIDE:

STARTING ADDRESS OF RUN
LENGTH DATA IS AN EVEN
ADDRESS

LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS
NUMBER OF 1 LINE BUFFER: 16 BYTES

NUMBER OF 1 LINE BUFFER: 16 BYTES

〔動作条件〕

動作条件
 メインメモリ側:ランレングスデータの開始アドレス 偶数アドレス
 ローカルメモリ側:イメージデータの開始アドレス 偶数アドレス

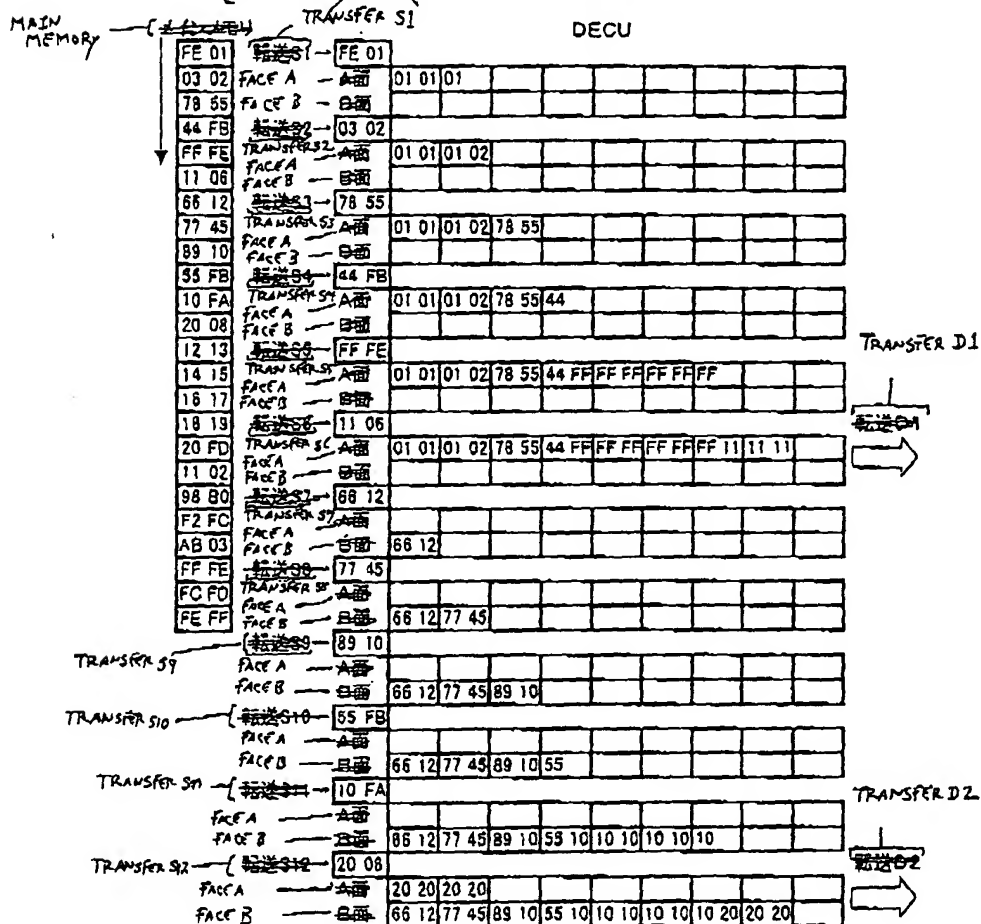
ローカルメモリ側、ページデータの開始アドレス 偶数アドレス

1ラインバイト数:16バイト

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榮所N6. EP9003

FIG. 7





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[illegible]

SETTING CONDITION

NO VERTICAL LINE REARRANGEMENT

TOTAL NUMBER OF DEVELOPED BYTES : 64 BYTES (16X4)

NUMBER OF BYTES IN 1 LINE : 16 BYTES

NUMBER OF DEVELOPED LINES : 4 LINES

頁社No. F008765
 発所No. EP9003

設定条件

ライン並び変換なし

総展開バイト数: 64バイト (16x4)

1ラインバイト数: 16バイト

展開ライン数: 4ライン

~~ローカルメモリ~~ → LOCAL MEMORY

FIG. 9A

① D1 →

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	11
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 9B

② D2 →

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	11
66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	20
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 9C

③ D3 →

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	11
66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	20
20	20	20	20	12	13	14	15
16	17	18	19	20	11	11	11
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 9D

④ D4 →

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	11
66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	20
20	20	20	20	12	13	14	15
16	17	18	19	20	11	11	11
11	98	30	F2	AB	AB	AB	AB
AB	FF	FE	FC	FD	FF	FF	FF

SETTING CONDITION
 VERTICAL LINE REARRANGEMENT PERFORMED
 TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES (16x4)
 NUMBER OF BYTES IN 1 LINE: 16 BYTES
 NUMBER OF DEVELOPED LINES: 4 LINES

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設定条件
 ライン並び変換あり
 総展開バイト数: 64バイト(16x4)
 1ラインバイト数: 16バイト
 展開ライン数: 4ライン

→ LOCAL MEMORY

FIG. 10A

D1 ↓

01 01	00 00	00 00	00 00	...	00 00
01 02	00 00	00 00	00 00	...	00 00
78 55	00 00	00 00	00 00	...	00 00
44 FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF 11	00 00	00 00	00 00	...	00 00
11 11	00 00	00 00	00 00	...	00 00

FIG. 10B

D2 ↓

01 01	66 12	00 00	00 00	...	00 00
01 02	77 45	00 00	00 00	...	00 00
78 55	89 10	00 00	00 00	...	00 00
44 FF	55 10	00 00	00 00	...	00 00
FF FF	10 10	00 00	00 00	...	00 00
FF FF	10 10	00 00	00 00	...	00 00
FF 11	10 20	00 00	00 00	...	00 00
11 11	20 20	00 00	00 00	...	00 00

FIG. 10C

D3 ↓

01 01	66 12	20 20	00 00	...	00 00
01 02	77 45	20 20	00 00	...	00 00
78 55	89 10	12 13	00 00	...	00 00
44 FF	55 10	14 15	00 00	...	00 00
FF FF	10 10	16 17	00 00	...	00 00
FF FF	10 10	18 19	00 00	...	00 00
FF 11	10 20	20 11	00 00	...	00 00
11 11	20 20	11 11	00 00	...	00 00

FIG. 10D

D4 ↓

01 01	66 12	20 20	11 98	...	00 00
01 02	77 45	20 20	B0 F2	...	00 00
78 55	89 10	12 13	ABAE	...	00 00
44 FF	55 10	14 15	ABAE	...	00 00
FF FF	10 10	16 17	AB FF	...	00 00
FF FF	10 10	18 19	FE FC	...	00 00
FF 11	10 20	20 11	FD FF	...	00 00
11 11	20 20	11 11	FF FF	...	00 00

OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA
IS AN ODD ADDRESS

LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA
IS AN EVEN ADDRESS

NUMBER OF 1 LINE BUFFER: 16 BYTES

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FIG. 11

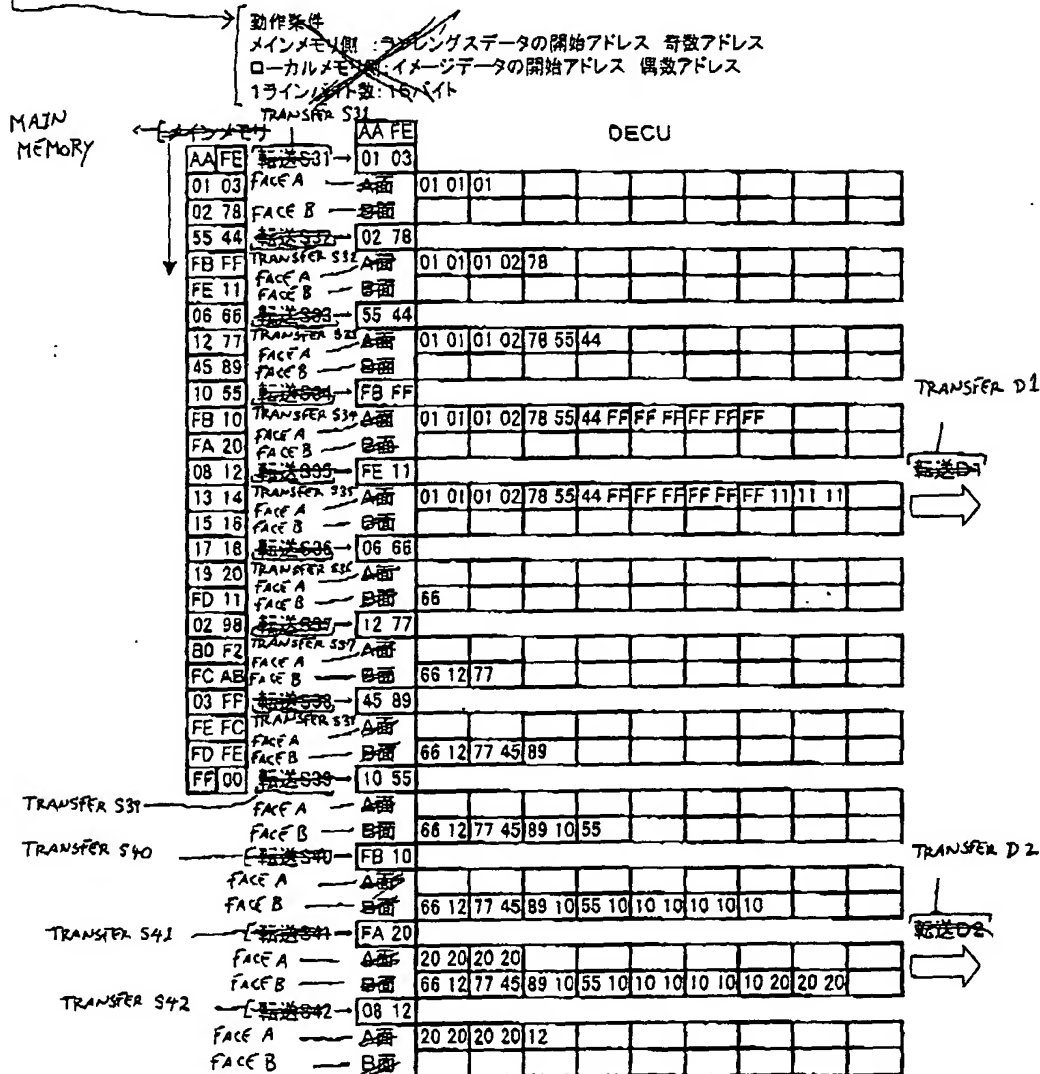
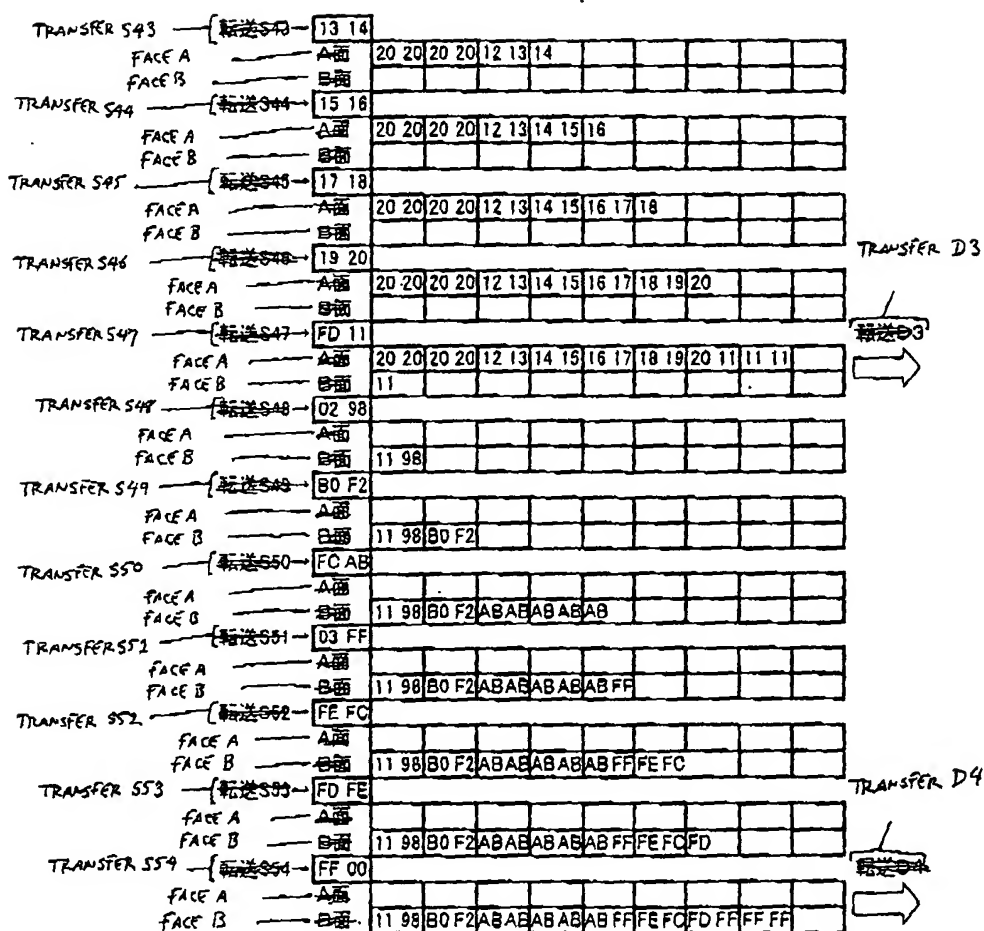


Fig. 12

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幣所No. EP9003

DECU



OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS

LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS

NUMBER OF 1 LINE BUFFER : 15 BYTES

FIG. 13

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動作条件

メインメモリ側：テンプレートの開始アドレス 偶数アドレス
ローカルメモリ側：イメージデータの開始アドレス 偶数アドレス
+ラインバイト数：15バイト

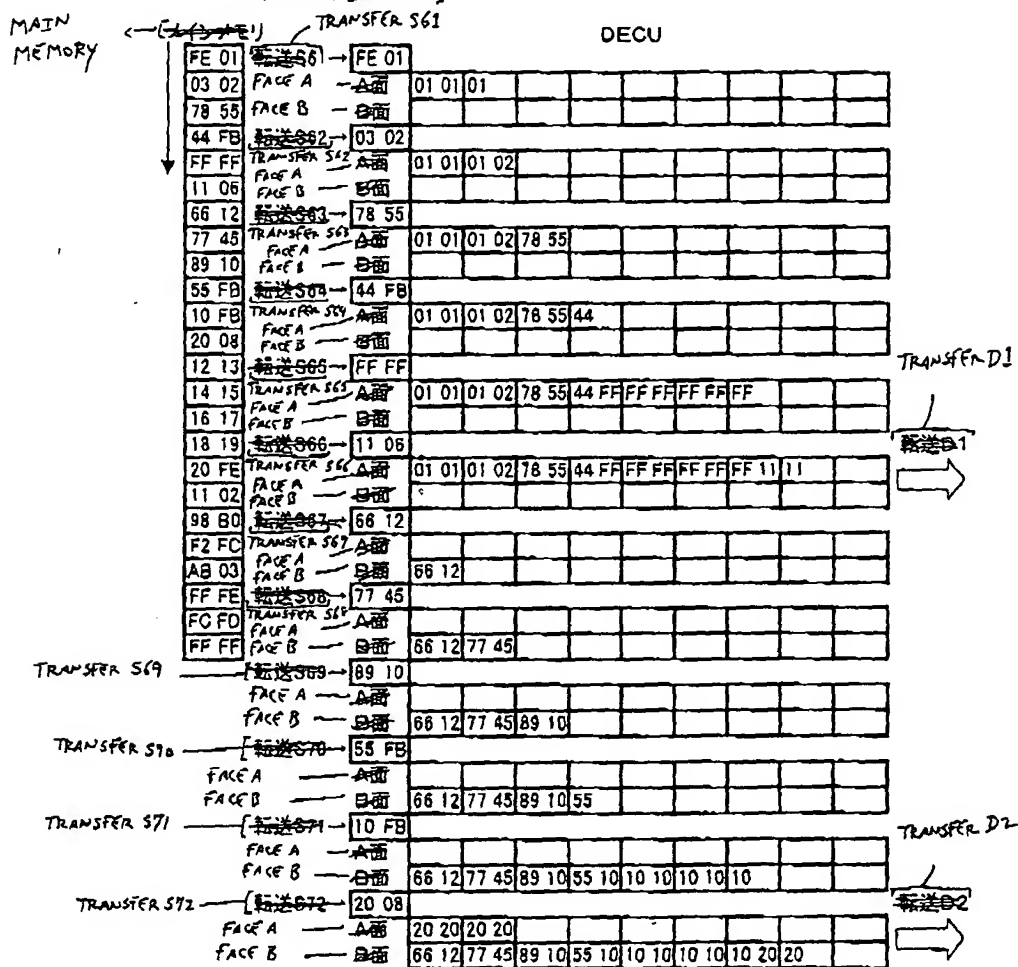
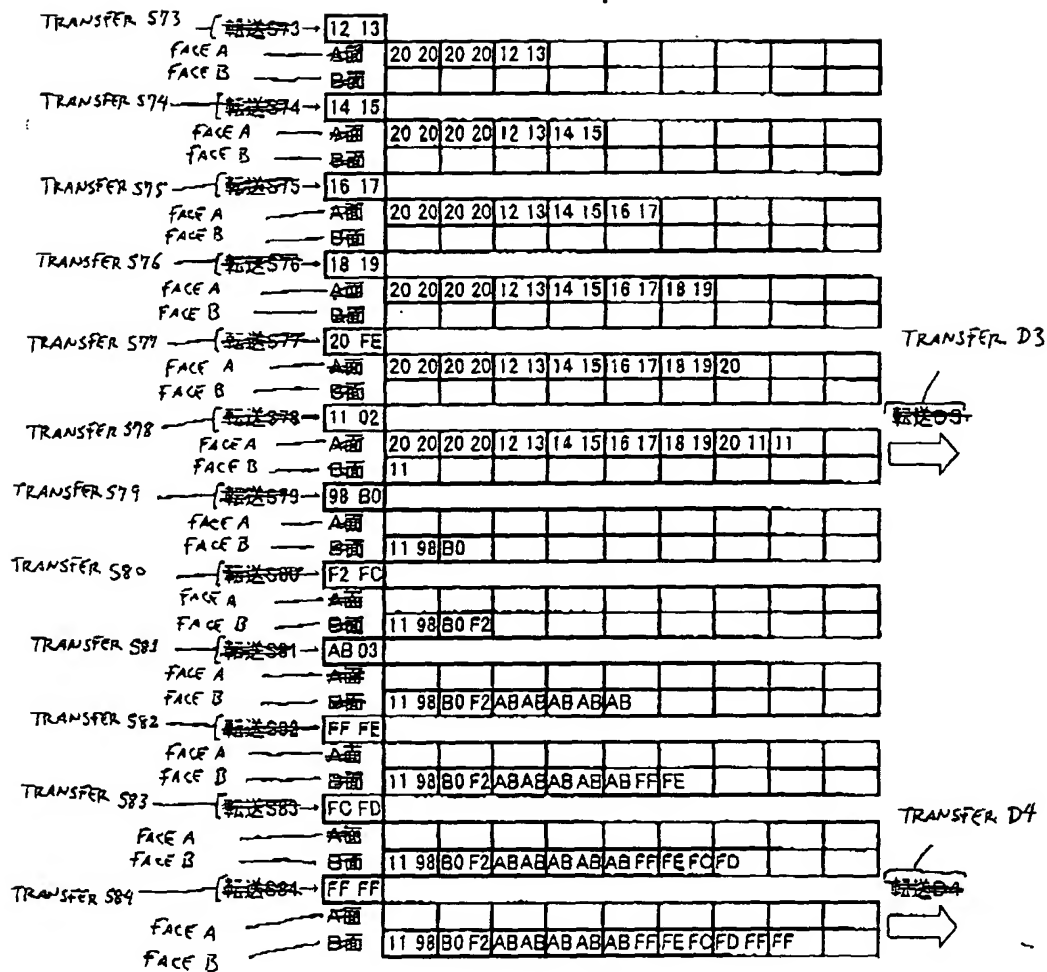


FIG. 14

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DECU



SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED

TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15x4)
NUMBER OF BYTES IN 1 LINE: 15 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

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図 15

設定条件

ライン並び替えあり
総展開バイト数: 60バイト(15x4)
1ラインバイト数: 15バイト
展開ライン数: 4ライン

ローカルメモリ → LOCAL MEMORY

(a)

FIG. 15A

D1 ↓

01 01	00 00	00 00	00 00	...	00 00
01 02	00 00	00 00	00 00	...	00 00
78 55	00 00	00 00	00 00	...	00 00
44 FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF 11	00 00	00 00	00 00	...	00 00
11 00	00 00	00 00	00 00	...	00 00

(b)

FIG. 15B

D2 ↓

01 01	66 12	00 00	00 00	...	00 00
01 02	77 45	00 00	00 00	...	00 00
78 55	89 10	00 00	00 00	...	00 00
44 FF	55 10	00 00	00 00	...	00 00
FF FF	10 10	00 00	00 00	...	00 00
FF FF	10 10	00 00	00 00	...	00 00
FF 11	10 20	00 00	00 00	...	00 00
11 00	20 00	00 00	00 00	...	00 00

(c)

FIG. 15C

D3 ↓

01 01	66 12	20 20	00 00	...	00 00
01 02	77 45	20 20	00 00	...	00 00
78 55	89 10	12 13	00 00	...	00 00
44 FF	55 10	14 15	00 00	...	00 00
FF FF	10 10	16 17	00 00	...	00 00
FF FF	10 10	18 19	00 00	...	00 00
FF 11	10 20	20 11	00 00	...	00 00
11 00	20 00	11 00	00 00	...	00 00

(d)

FIG. 15D

D4 ↓

01 01	66 12	20 20	11 98	...	00 00
01 02	77 45	20 20	B0 F2	...	00 00
78 55	89 10	12 13	ABAE	...	00 00
44 FF	55 10	14 15	ABAE	...	00 00
FF FF	10 10	16 17	AB FF	...	00 00
FF FF	10 10	18 19	FE FC	...	00 00
FF 11	10 20	20 11	FD FF	...	00 00
11 00	20 00	11 00	FF 00	...	00 00

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設定条件
 ライン縦並び変換なし
 総展開バイト数: 60バイト(15×4)
 1ラインバイト数: 15バイト
 展開ライン数: 4ライン

SETTING CONDITION
 NO VERTICAL LINE REARRANGEMENT
 TOTAL NUMBER OF DEVELOPED
 BYTES: 60 BYTES (15×4)
 NUMBER OF BYTES IN 1 LINE: 15 BYTES
 NUMBER OF DEVELOPED LINES: 4 LINES

LOCAL MEMORY

FIG. 16A

④ D1→

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 16B

④ D2→

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	00
66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 16C

④ D3→

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	00
66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	00
20	20	20	20	12	13	14	15
16	17	18	19	20	11	11	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 16D

④ D4→

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	00
66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	00
20	20	20	20	12	13	14	15
16	17	18	19	20	11	11	00
11	98	B0	F2	AB	AB	AB	AB
AB	FF	FE	FD	FD	FF	FF	00

FIG. 17

OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN ODD ADDRESS

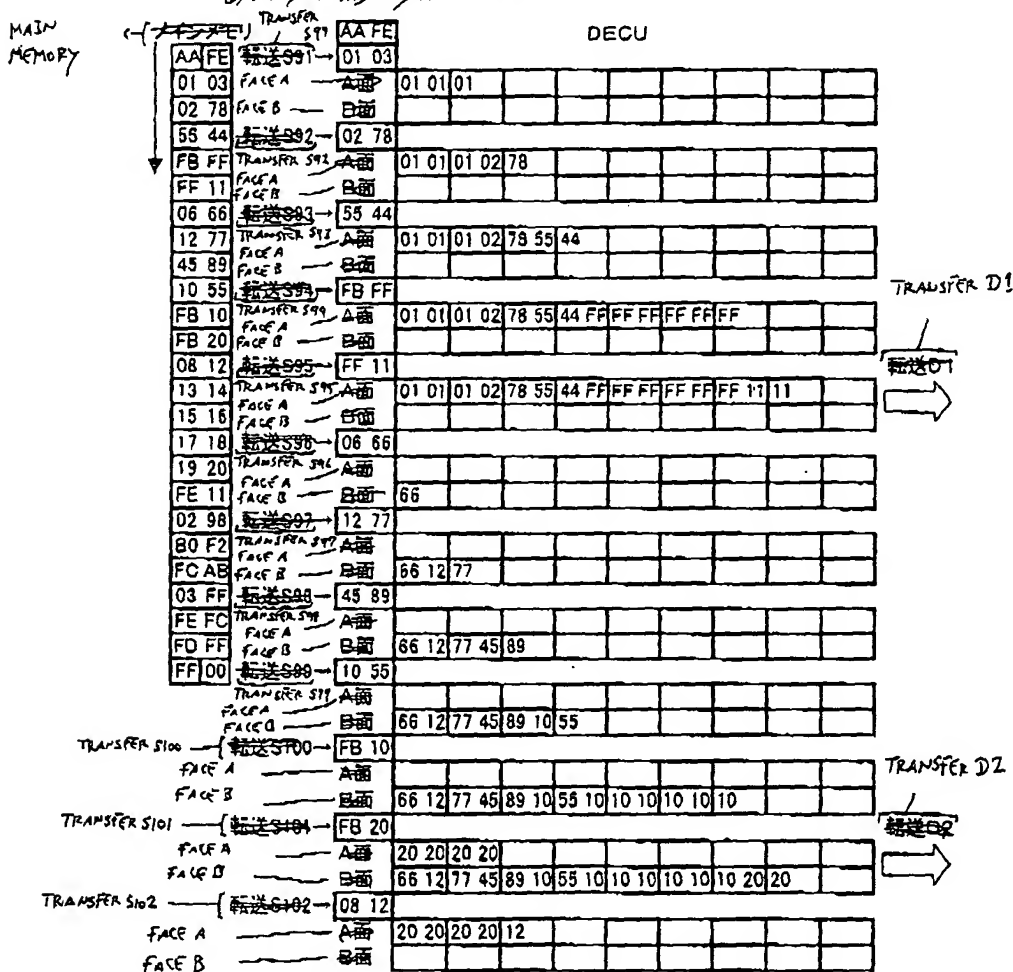
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS

NUMBER OF 1 LINE BUFFER: 15 BYTES

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動作条件

メインメモリ側: テンプレートの開始アドレス 奇数アドレス
ローカルメモリ側: イメージデータの開始アドレス 偶数アドレス
1ラインバイト数 5バイト



OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS

LOCAL MEMORY SIZE: STARTING ADDRESS OF IMAGE DATA IS AN ODD ADDRESS

NUMBER OF 1 LINE BUFFER: 16 BYTES

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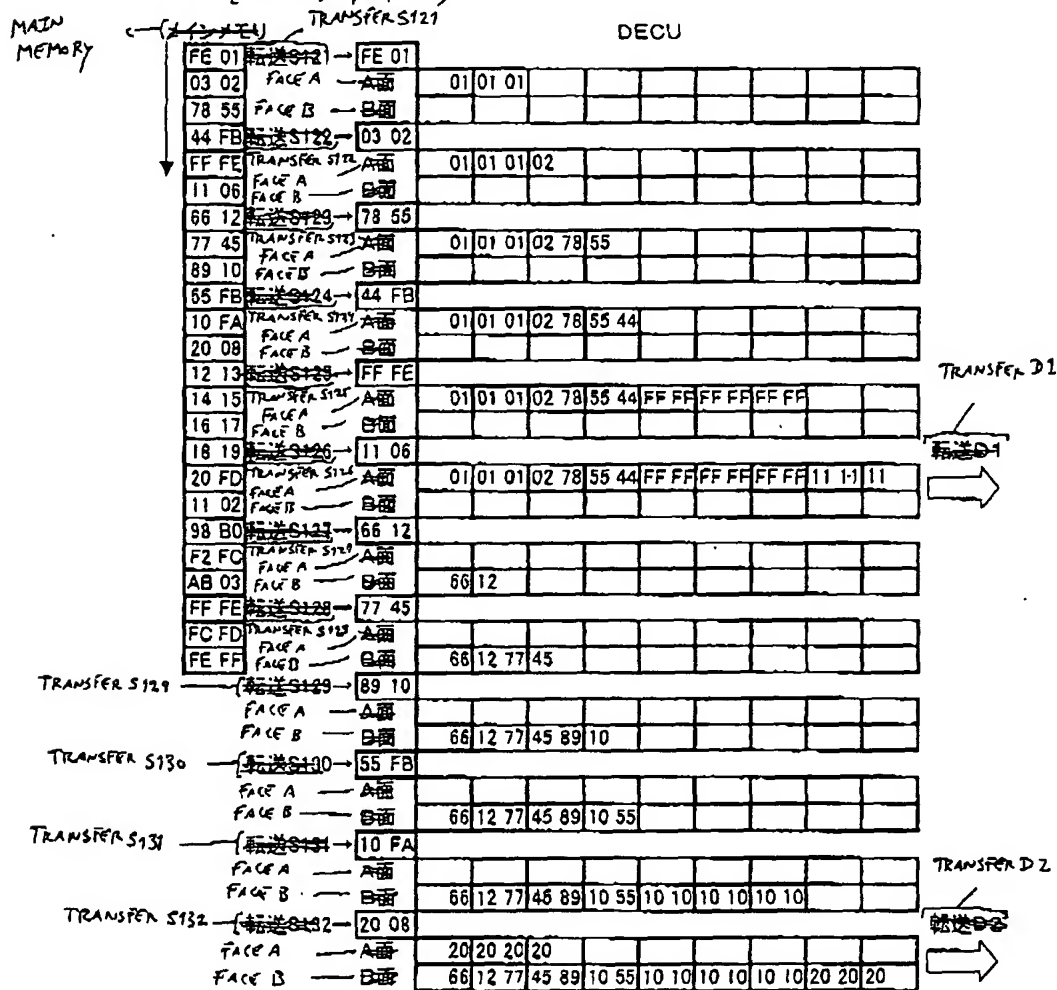
FIG. 19

動作要件

メインメモリ側：ラフレングスデータの開始アドレス 偶数アドレス
ローカルメモリ側：イメージデータの開始アドレス 奇数アドレス
1ラインバイト数：16バイト

ローカルメモリ側: イメージデータの開始アドレス 奇数アドレス

イテインバイト数: 26バイト



SETTING CONDITION

VERTICAL LINE REARRANGEMENT PERFORMED

TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES (16x4)

NUMBER OF BYTES IN 1 LINE: 16 BYTES

NUMBER OF DEVELOPED LINES: 4 LINES

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設定条件

ライン並び変換あり

総展開バイト数: 64バイト (16x4)

1ラインバイト数: 16バイト

展開ライン数: 4ライン

LOCAL MEMORY

FIG. 21A

D1 ↓

00	01	00	00	00	00	00	00	...	00	00
01	01	00	00	00	00	00	00	...	00	00
02	78	00	00	00	00	00	00	...	00	00
55	44	00	00	00	00	00	00	...	00	00
FF	FF	00	00	00	00	00	00	...	00	00
FF	FF	00	00	00	00	00	00	...	00	00
FF	FF	00	00	00	00	00	00	...	00	00
11	11	00	00	00	00	00	00	...	00	00
11	00	00	00	00	00	00	00	...	00	00

FIG. 21B

D2 ↓

00	01	00	68	00	00	00	00	...	00	00
01	01	12	77	00	00	00	00	...	00	00
02	78	45	89	00	00	00	00	...	00	00
55	44	10	55	00	00	00	00	...	00	00
FF	FF	10	10	00	00	00	00	...	00	00
FF	FF	10	10	00	00	00	00	...	00	00
FF	FF	10	10	00	00	00	00	...	00	00
11	11	20	20	00	00	00	00	...	00	00
11	00	20	00	00	00	00	00	...	00	00

FIG. 21C

D3 ↓

00	01	00	66	00	20	00	00	...	00	00
01	01	12	77	20	20	00	00	...	00	00
02	78	45	89	20	12	00	00	...	00	00
55	44	10	55	13	14	00	00	...	00	00
FF	FF	10	10	15	16	00	00	...	00	00
FF	FF	10	10	17	18	00	00	...	00	00
FF	FF	10	10	19	20	00	00	...	00	00
11	11	20	20	11	11	00	00	...	00	00
11	00	20	00	11	00	00	00	...	00	00

FIG. 21D

D4 ↓

00	01	00	66	00	20	00	11	...	00	00
01	01	12	77	20	20	98	B0	...	00	00
02	78	45	89	20	12	F2	AE	...	00	00
55	44	10	55	13	14	AB	AE	...	00	00
FF	FF	10	10	15	16	AB	AE	...	00	00
FF	FF	10	10	17	18	FF	FE	...	00	00
FF	FF	10	10	19	20	FC	FD	...	00	00
11	11	20	20	11	11	FF	FF	...	00	00
11	00	20	00	11	00	FF	00	...	00	00

OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS

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LOCAL MEMORY SIZE: STARTING ADDRESS OF IMAGE DATA IS AN ODD ADDRESS ~~22~~

FIG. 22

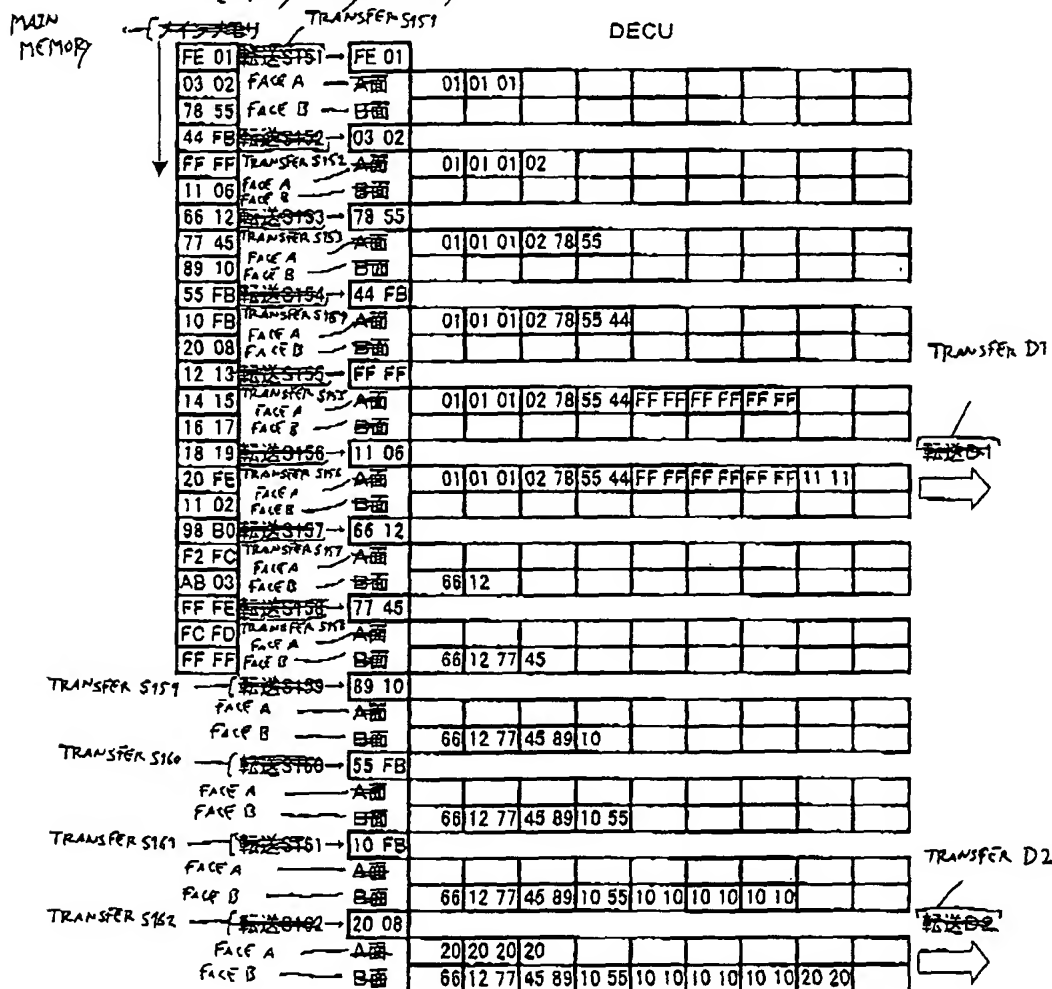
NUMBER OF 1 LINE BUFFER: 15 BYTES

动作要领

メインメモリ: ランダムアクセスメモリの開始アドレス 偶数アドレス

ローカルメモリ: イメージデータの開始アドレス 奇数アドレス

ラインバイト数:15バイト



SETTING CONDITION.

VERTICAL LINE REARRANGEMENT PERFORMED

TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15X4)

NUMBER OF BYTES IN 1 LINE: 15 BYTES

NUMBER OF DEVELOPED LINES: 4 LINES

貴社No. F008765
発所No. 6P9003

設定条件

ライン繰並び変換あり

総展開バイト数: 60バイト (15×4)

1ラインバイト数: 15バイト

展開ライン数: 4ライン

LOCAL MEMORY

key
FIG. 24A

D1 ↓

00 01	00 00	00 00	00 00	...	00 00
01 01	00 00	00 00	00 00	...	00 00
02 78	00 00	00 00	00 00	...	00 00
55 44	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
11 11	00 00	00 00	00 00	...	00 00

key
FIG. 24B

D2 ↓

00 01	00 66	00 00	00 00	...	00 00
01 01	12 77	00 00	00 00	...	00 00
02 78	45 89	00 00	00 00	...	00 00
55 44	10 55	00 00	00 00	...	00 00
FF FF	10 10	00 00	00 00	...	00 00
FF FF	10 10	00 00	00 00	...	00 00
FF FF	10 10	00 00	00 00	...	00 00
11 11	20 20	00 00	00 00	...	00 00

key
FIG. 24C

D3 ↓

00 01	00 66	00 20	00 00	...	00 00
01 01	12 77	20 20	00 00	...	00 00
02 78	45 89	20 12	00 00	...	00 00
55 44	10 55	13 14	00 00	...	00 00
FF FF	10 10	15 16	00 00	...	00 00
FF FF	10 10	17 18	00 00	...	00 00
FF FF	10 10	19 20	00 00	...	00 00
11 11	20 20	11 11	00 00	...	00 00

key
FIG. 24D

D4 ↓

00 01	00 66	00 20	00 11	...	00 00
01 01	12 77	20 20	98 B0	...	00 00
02 78	45 89	20 12	F2 AB	...	00 00
55 44	10 55	13 14	AB AB	...	00 00
FF FF	10 10	15 16	AB AB	...	00 00
FF FF	10 10	17 18	FF FE	...	00 00
FF FF	10 10	19 20	FC FD	...	00 00
11 11	20 20	11 11	FF FF	...	00 00

FIG. 26

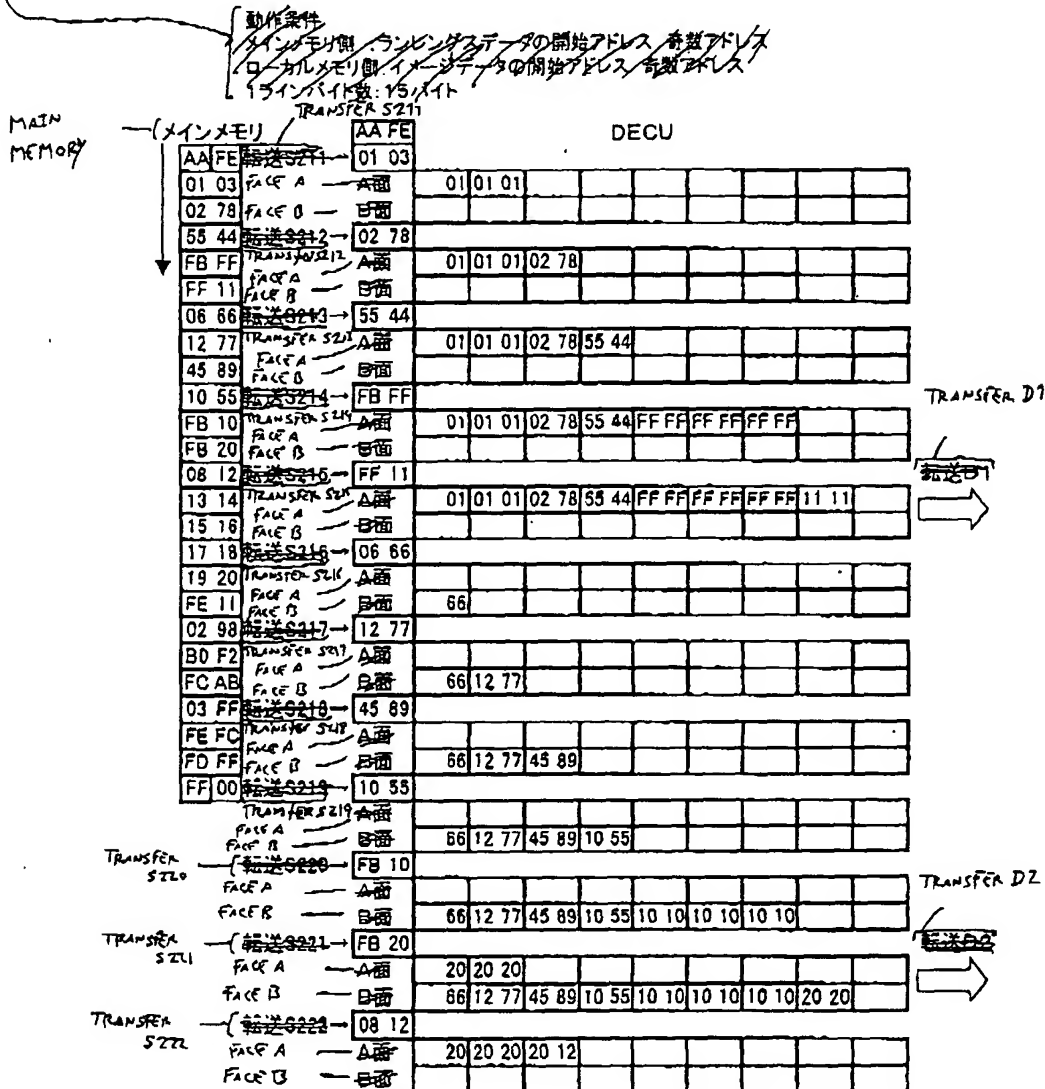
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DECU

TRANSFER	Transfer ID	FACE A	FACE B	13	14	20	20	20	12	13	14							
TRANSFER 5193	(転送5193)	FACE A	前面			20	20	20	12	13	14							
		FACE B	后面															
TRANSFER 5194	(転送5194)	FACE A	前面			20	20	20	12	13	14	15	16					
		FACE B	后面															
TRANSFER 5195	(転送5195)	FACE A	前面			20	20	20	12	13	14	15	16	17	18			
		FACE B	后面															
TRANSFER 5196	(転送5196)	FACE A	前面			20	20	20	12	13	14	15	16	17	18	19	20	
		FACE B	后面															
TRANSFER 5197	(転送5197)	FACE A	前面			20	20	20	12	13	14	15	16	17	18	19	20	11 11 11
		FACE B	后面			11												
TRANSFER 5198	(転送5198)	FACE A	前面															
		FACE B	后面			11	98											
TRANSFER 5199	(転送5199)	FACE A	前面															
		FACE B	后面			11	98	B0	F2									
TRANSFER 5200	(転送5200)	FACE A	前面															
		FACE B	后面			11	98	B0	F2	AB	AB	AB	AB	AB	AB			
TRANSFER 5201	(転送5201)	FACE A	前面															
		FACE B	后面			11	98	B0	F2	AB	AB	AB	AB	AB	FF			
TRANSFER 5202	(転送5202)	FACE A	前面															
		FACE B	后面			11	98	B0	F2	AB	AB	AB	AB	AB	FF	FE	FC	
TRANSFER 5203	(転送5203)	FACE A	前面															
		FACE B	后面			11	98	B0	F2	AB	AB	AB	AB	AB	FF	FE	FC	FD
TRANSFER 5204	(転送5204)	FACE A	前面															
		FACE B	后面			11	98	B0	F2	AB	AB	AB	AB	AB	FF	FE	FC	FD

OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH
DATA IS AN ODD ADDRESSLOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA
IS AN ODD ADDRESSNUMBER OF 1 LINE BUFFER: 15 BYTES ~~図 27~~ FIG. 27貴社No. F008765
特許No. EP9003

SETTING CONDITION
 VERTICAL LINE REARRANGEMENT PERFORMED
 TOTAL NUMBER OF DEVELOPED BYTES : 64 BYTES (16X4)
 NUMBER OF BYTES IN 1 LINE : 16 BYTES
 NUMBER OF DEVELOPED LINES : 4 LINES

資社No. F008765
 発所No. EP9003

設定条件
 ライン並び変換あり
 総展開バイト数: 64バイト(16×4)
 1ラインバイト数: 16バイト
 展開ライン数: 4ライン

LOCAL MEMORY

FIG. 29A

011 ~~11~~ → 11 IMAGE 1

01 01	00 00	00 00	00 00	...	00 00
01 02	00 00	00 00	00 00	...	00 00
78 55	00 00	00 00	00 00	...	00 00
44 FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF 11	00 00	00 00	00 00	...	00 00
11 11	00 00	00 00	00 00	...	00 00

FIG. 29B

021 ~~11~~ → 12 IMAGE 2

66 12	00 00	00 00	00 00	...	00 00
77 45	00 00	00 00	00 00	...	00 00
89 10	00 00	00 00	00 00	...	00 00
55 10	00 00	00 00	00 00	...	00 00
10 10	00 00	00 00	00 00	...	00 00
10 10	00 00	00 00	00 00	...	00 00
10 20	00 00	00 00	00 00	...	00 00
20 20	00 00	00 00	00 00	...	00 00

FIG. 29C

031 ~~11~~ → 13 IMAGE 1

01 01	20 20	00 00	00 00	...	00 00
01 02	20 20	00 00	00 00	...	00 00
78 55	12 13	00 00	00 00	...	00 00
44 FF	14 15	00 00	00 00	...	00 00
FF FF	16 17	00 00	00 00	...	00 00
FF FF	18 19	00 00	00 00	...	00 00
FF 11	20 11	00 00	00 00	...	00 00
11 11	11 11	00 00	00 00	...	00 00

FIG. 29D

041 ~~11~~ → 14 IMAGE 2

66 12	11 98	00 00	00 00	...	00 00
77 45	B0 F2	00 00	00 00	...	00 00
89 10	AB AB	00 00	00 00	...	00 00
55 10	AB AB	00 00	00 00	...	00 00
10 10	AB FF	00 00	00 00	...	00 00
10 10	FE FC	00 00	00 00	...	00 00
10 20	FD FF	00 00	00 00	...	00 00
20 20	FF FF	00 00	00 00	...	00 00

SETTING CONDITION
 NO VERTICAL LINE REARRANGEMENT
 TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES (16x4)
 NUMBER OF BYTES IN 1 LINE: 16 BYTES
 NUMBER OF DEVELOPED LINES: 4 LINES

貴社No. F008765
 発所No. EP9003

設定条件
 ライン並び変換なし
 総展開バイト数: 64バイト(16x4)
 1ラインバイト数: 16バイト
 展開ライン数: 4ライン

FIG. 30A

(a) D1 →

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

LOCAL MEMORY
 IMAGE 1

FIG. 30B

(b) D2 →

66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

IMAGE 2

FIG. 30C

(c) D3 →

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
20 20	20 20	12 13	14 15
16 17	18 19	20 11	11 11
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

IMAGE 1

FIG. 30D

(d) D4 →

66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
11 98	90 F2	AB AB	AB AB
AB FF	FE FC	FD FF	FF FF
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

IMAGE 2

SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15X4)
NUMBER OF BYTES IN 1 LINE: 15 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

貢社No. F008765
幣所No. EP9003

~~設定条件
ライン繰並び変換あり
総展開バイト数: 50バイト(15×4)
1ラインバイト数: 15バイト
展開ライン数: 4ライン~~

FIG. 31A

FIG. 31B

FIG. 31C

FIG. 37D

LOCAL MEMORY

- IMAGE 1

- IMAGE 2

IMAGE]

- IMAGE 2

SETTING CONDITION

NO VERTICAL LINE REARRANGEMENT

TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15X4)

NUMBER OF BYTES IN 1 LINE: 15 BYTES

NUMBER OF DEVELOPED LINES: 4 LINES

貴社 No. EP08765
第 所 No. EP9003

設定条件

ライン並び変換なし

総展開バイト数: 60バイト (15x4)

1ラインバイト数: 15バイト

展開ライン数: 4ライン

FIG. 32A

LOCAL MEMORY
IMAGE 1

D1-

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 32B

IMAGE 2

D2-

66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 32C

IMAGE 1

D3-

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	00
20	20	20	20	12	13	14	15
16	17	18	19	20	11	11	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 32D

IMAGE 2

D4-

66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	00
11	98	80	F2	ABA	BAB	AB	AB
AB	FF	FE	FC	FD	FF	FF	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES (16x4)
NUMBER OF BYTES IN 1 LINE: 16 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

貴社No. F008765
幣所No. EP9003

~~設定条件
ライン縦並び変換あり
縫展開バイト数:64バイト(16×4)
1ラインバイト数:16バイト
展開ライン数:4ライン~~

FIG. 33A

FIG. 33B

FIG. 33C

FIG. 33D

LOCAL MEMORY

D11

00	01	00 00	00 00	00 00	00 00	00 00
01	01	00 00	00 00	00 00	00 00	00 00
02	78	00 00	00 00	00 00	00 00	00 00
55	44	00 00	00 00	00 00	00 00	00 00
FF	FF	00 00	00 00	00 00	00 00	00 00
FF	FF	00 00	00 00	00 00	00 00	00 00
FF	FF	00 00	00 00	00 00	00 00	00 00
11	11	00 00	00 00	00 00	00 00	00 00
11	00	00 00	00 00	00 00	00 00	00 00

D21

00	68	00 00	00 00	00 00	00 00	00 00
12	77	00 00	00 00	00 00	00 00	00 00
45	89	00 00	00 00	00 00	00 00	00 00
10	55	00 00	00 00	00 00	00 00	00 00
10	10	00 00	00 00	00 00	00 00	00 00
10	10	00 00	00 00	00 00	00 00	00 00
10	10	00 00	00 00	00 00	00 00	00 00
20	20	00 00	00 00	00 00	00 00	00 00
20	00	00 00	00 00	00 00	00 00	00 00

D31

00	01	00 20	00 00	00 00	00 00	00 00
01	01	20 20	00 00	00 00	00 00	00 00
02	78	20 12	00 00	00 00	00 00	00 00
55	44	13 14	00 00	00 00	00 00	00 00
FF	FF	15 16	00 00	00 00	00 00	00 00
FF	FF	17 18	00 00	00 00	00 00	00 00
FF	FF	19 20	00 00	00 00	00 00	00 00
11	11	11 11	00 00	00 00	00 00	00 00
11	00	11 00	00 00	00 00	00 00	00 00

D41

00	68	00 11	00 00	00 00	00 00	00 00
12	77	98 B0	00 00	00 00	00 00	00 00
45	89	F2 A8	00 00	00 00	00 00	00 00
10	55	AB AE	00 00	00 00	00 00	00 00
10	10	AB AE	00 00	00 00	00 00	00 00
10	10	FF FE	00 00	00 00	00 00	00 00
10	10	FC FD	00 00	00 00	00 00	00 00
20	20	FF FF	00 00	00 00	00 00	00 00
20	00	FF 00	00 00	00 00	00 00	00 00

SETTING CONDITION

VERTICAL LINE REARRANGEMENT PERFORMED

TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15X4)

NUMBER OF BYTES IN 1 LINE: 15 BYTES

NUMBER OF DEVELOPED LINES: 4 LINES

資社No. P008765
 弊所No. EP9003

設定条件
 ライン並び(変更あり)
 総展開バイト数: 60バイト(15x4)
 1ラインバイト数: 15バイト
 展開ライン数: 4ライン

FIG. 34A

LOCAL MEMORY

D1 1

00	01	00	00	00	00	00	00	...	00	00
01	01	00	00	00	00	00	00	...	00	00
02	78	00	00	00	00	00	00	...	00	00
55	44	00	00	00	00	00	00	...	00	00
FF	FF	00	00	00	00	00	00	...	00	00
FF	FF	00	00	00	00	00	00	...	00	00
FF	FF	00	00	00	00	00	00	...	00	00
11	11	00	00	00	00	00	00	...	00	00

IMAGE 1

FIG. 34B

D2 1

00	66	00	00	00	00	00	00	...	00	00
12	77	00	00	00	00	00	00	...	00	00
45	89	00	00	00	00	00	00	...	00	00
10	55	00	00	00	00	00	00	...	00	00
10	10	00	00	00	00	00	00	...	00	00
10	10	00	00	00	00	00	00	...	00	00
10	10	00	00	00	00	00	00	...	00	00
20	20	00	00	00	00	00	00	...	00	00

IMAGE 2

FIG. 34C

D3 1

00	01	00	20	00	00	00	00	...	00	00
01	01	20	20	00	00	00	00	...	00	00
02	78	20	12	00	00	00	00	...	00	00
55	44	13	14	00	00	00	00	...	00	00
FF	FF	15	16	00	00	00	00	...	00	00
FF	FF	17	18	00	00	00	00	...	00	00
FF	FF	19	20	00	00	00	00	...	00	00
11	11	11	11	00	00	00	00	...	00	00

IMAGE 1

FIG. 34D

D4 1

00	66	00	11	00	00	00	00	...	00	00
12	77	98	BC	00	00	00	00	...	00	00
45	89	F2	AB	00	00	00	00	...	00	00
10	55	AB	AB	00	00	00	00	...	00	00
10	10	AB	AB	00	00	00	00	...	00	00
10	10	FF	FE	00	00	00	00	...	00	00
10	10	FC	FD	00	00	00	00	...	00	00
20	20	FF	FF	00	00	00	00	...	00	00

IMAGE 2

FIG. 35

OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS

LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS

頁 No. F008765
第 所 No. EP9003

動作条件
メインメモリ側: イメージデータの開始アドレス 偶数アドレス
ローカルメモリ側: イメージデータの開始アドレス 偶数アドレス
ラインサイズ: 16バイト

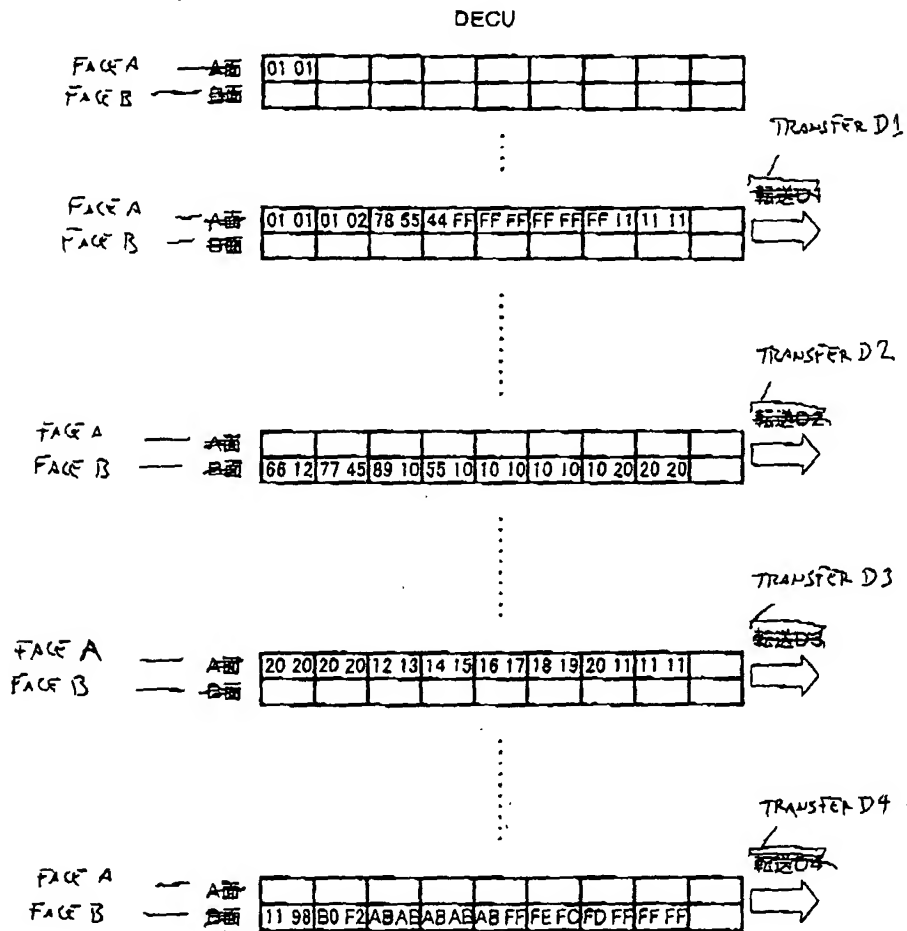


FIG. 36

~~特許No. F008765~~

~~特許No. EP9003~~

